

RVT101HVLNWC00-B

HB, IPS LVDS 10.1" LCD TFT DATASHEET

Rev.1.4 2023-01-19

ІТЕМ	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally black/IPS	/
Size	10.1	Inch
Viewing Direction	Free	/
Outside Dimensions (W x H x D)	257.96 x 168.60 x 9.63	mm
Active Area (W x H)	216.96 x 135.60	mm
Pixel Pitch (W x H)	0.1695 x 0.1695	mm
Resolution	1280 (RGB) x 800	/
Brightness	850	cd/m²
LCD Interface Type	LVDS	/
Color Depth	16.7M	/
Pixel Arrangement	RGB Vertical Stripe	/
LCD Driver	EK79202B	/
With/Without Touch	With Projected Capacitive Touch Panel	/
CTP Driver	ILI2132A	/
Touch Interface Type	USB /I2C/ Optional UART	/
Bonding Technology	Optical Bonding	/
Weight	440	g

Note 1. RoHS3 compliant

Note 2. LCM weight tolerance: ± 5%.



1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2020-10-22	Initial Release	
1.1	2021-03-08	Dimension overhaul	
1.2	2021-05-26	Modify Electrical Specification and power on/off sequence Correction on touch IC PN	
1.3	2021-07-28	Updating new template Correcting the Operating/standby current From: Operating Current knows.av - 15 20 mA Standby Current krows.av - 250 uA To: Operating Current knows.av - 280 420 mA Standby Current krows.av - 15 2.0 mA	
1.4	2023-01-19	Modify DE signal: Active High	



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3. MODULE CLASSIFICATION INFORMATION

		101								
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.	

NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	101 – 10.1"
4.	MODEL SERIAL NO.	H – High Brightness, IPS
5.	RESOLUTION	V – 1280 x 800 px
6.	INTERFACE	L – TFT LCD, LVDS
7.	FRAME	N – Without Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	C – With Capacitive Touch Panel
10.	VERSION	00 – (00-99)
11.	BONDING TECHNOLOGY	B – Optical Bonding

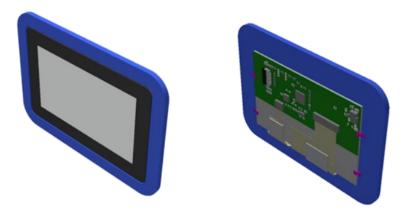


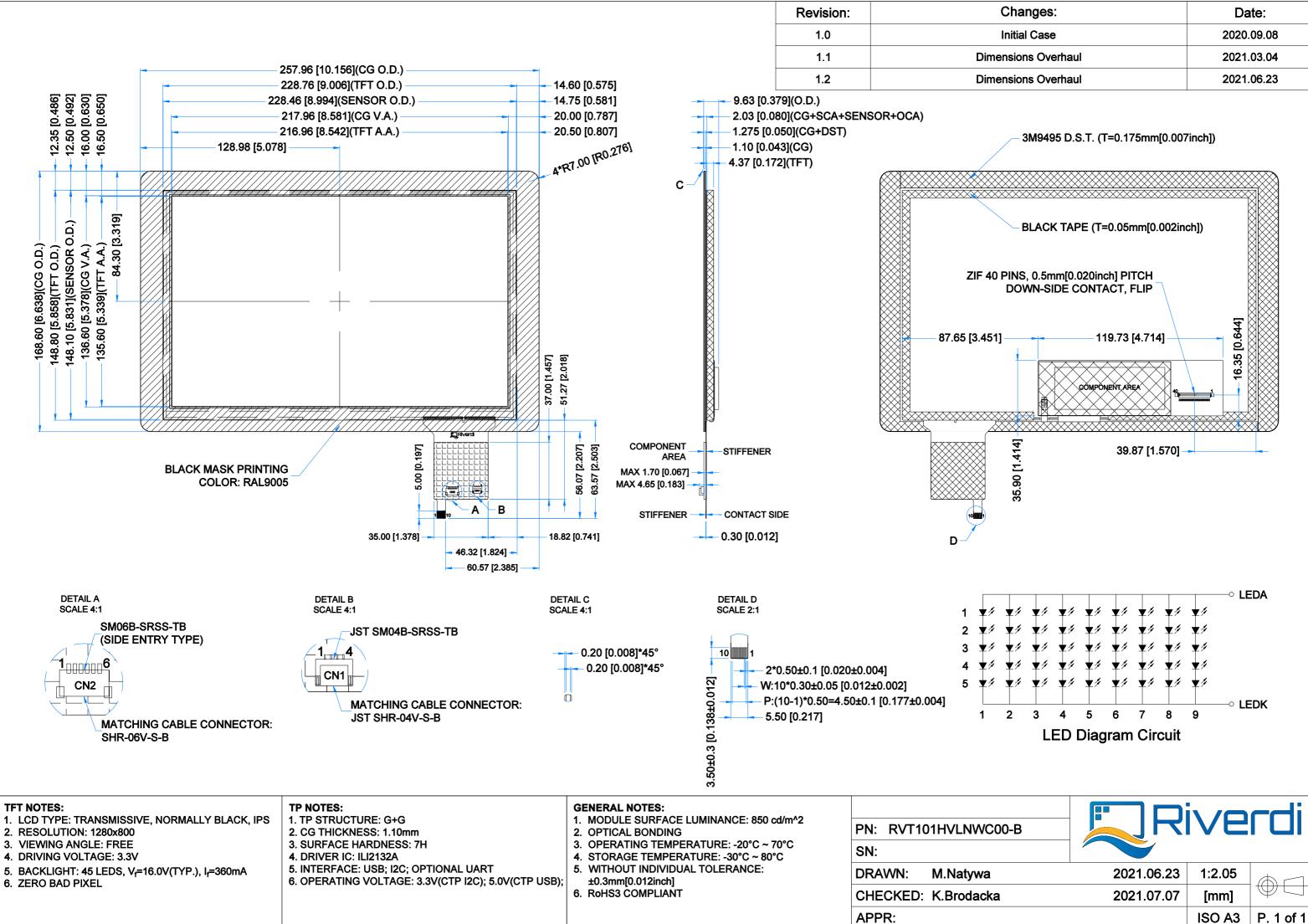
4. uxTouch ASSEMBLY

uxTouch are LCD TFT displays with specially designed projected capacitive touch panels. uxTouch display can be mounted without any additional holes in the housing. Our standard uxTouch displays include double-sided adhesive tape (DST) to stick TFT easily to the housing.

uxTouch models with double-side adhesive tape can be mounted by fastening the glass to the housing.

Figure 1. General view of the module





Changes:	Date:
Initial Case	2020.09.08
nensions Overhaul	2021.03.04
nensions Overhaul	2021.06.23



6. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage for Module	VDD	-0.3	3.9	V
Operating Temperature	T _{OP}	-20	70	٥٢
Storage Temperature	T _{ST}	-30	80	C

Note 1. The absolute maximum rating values must not be exceeded at any times. The module MUST NOT be used when any of the absolute maximum ratings is exceeded.

The characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

7. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage	V _{DD}	2.6	3.3	3.6	V
Operating Current	I _{VDD=3.3V}	-	280	420	mA
Standby Current	I _{ST}	-	1.5	2.0	mA

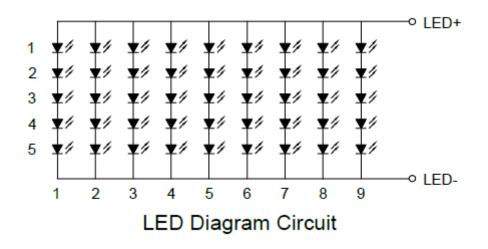
8. BACKLIGHT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Backlight Driving Voltage	VF	15.0	16.0	17.0	V	
Backlight Driving Current	I _F	315	360	405	mA	
Backlight Power Consumption	W _{BL}	-	5760	-	mW	
LED Lifetime	-	-	50,000	-	hours	Note 1

Note 1. Each LED: I_F =40 mA, V_F=3.2 ±0.2V.

Note 2. Optical performance should be evaluated at T_a =25 °C only.

Note 3. Operating life means the period in which the LED brightness goes down to 50% of the initial brightness. Typical operating lifetime is the estimated parameter.





9. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE	
Response Time	Tr+Tf		-	25	35	ms	FIG 2.	4	
Contrast Ratio	Cr	θ=O°	800	1000	-			1	
Luminance Uniformity	δ WHITE	ø=0° Ta=25 °C	-	75	-	%	FIG 3	3	
Surface Luminance	Lv	Ta=25 °C	-	850	-	cd/m²		2	
		ø = 90°	75	85	-	deg			
Viewing Angle	е е — А	ø = 270°	75	85	-	deg	FIG 4.	6	
Range		U	ge	ø = 0°	75	85	-	deg	
		ø = 180°	75	85	-	deg			
	Rx		0.22	0.26	0.30	-			
	Ry		0.20	0.24	0.28	-			
	Gx	θ=O°	0.34	0.38	0.42	-			
CIE (x, y)	Gy	ø=0°	0.50	0.54	0.58	-	FIG 3.	5	
Chromaticity	Bx		0.10	0.14	0.18	-	FIU J.		
	By	1d-25 C	0.09	0.13	0.17	-			
	Wx		0.28	0.32	0.36	-			
	Wy		0.29	0.33	0.37	-			

Note 1. Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 3.

Contrast Ratio = $\frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 3.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 3.

 $\delta \text{ WHITE } = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 2. The test equipment is BM-7A.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 4.



Note 7. Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80). For response time testing, the testing data is based on BM-7A. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, Chromaticity the test data is based on SR-3A.

Figure 2. The definition of response time

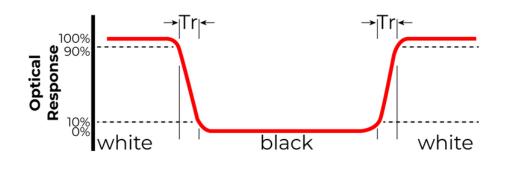


Figure 3. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

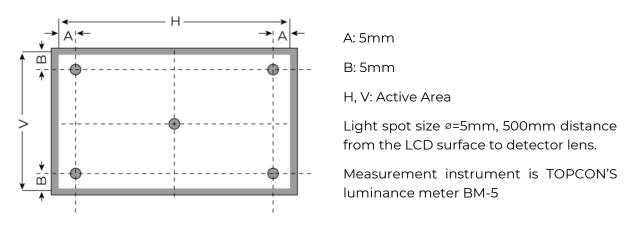
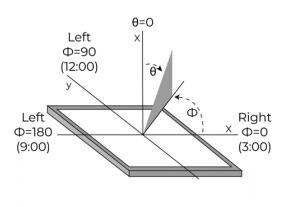


Figure 4. The definition of viewing angle





10. INTERFACES DESCRIPTION

10.1 TFT assignment

Matched Riverdi ZIF connector: ZIF0540DH-CF25

1NC-No Connection2VDDPPower Supply, 3.3V3VDDPPower Supply, 3.3V4-6NC-No Connection7GNDPGround8Rxin0-I-LVDS Differential Data Input9Rxin0+I+LVDS Differential Data Input10GNDPGround11Rxin1+I+LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input23NC-No Connection24NC-No Connection	3 4-6 7 8 9 10 11
3VDDPPower Supply, 3.3V4-6NC-No Connection7GNDPGround8Rxin0-I-LVDS Differential Data Input9Rxin0+I+LVDS Differential Data Input10GNDPGround11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	3 4-6 7 8 9 10 11
4-6NC-No Connection7GNDPGround8Rxin0-I-LVDS Differential Data Input9Rxin0+I+LVDS Differential Data Input10GNDPGround11Rxin1+I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	4-6 7 8 9 10 11
7GNDPGround8Rxin0-I-LVDS Differential Data Input9Rxin0+I+LVDS Differential Data Input10GNDPGround11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	7 8 9 10 11
8Rxin0-I-LVDS Differential Data Input9Rxin0+I+LVDS Differential Data Input10GNDPGround11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	8 9 10 11
9RxinO+I+LVDS Differential Data Input10GNDPGround11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input23NC-No Connection	9 10 11
10GNDPGround11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I-LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I-LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	10 11
11Rxin11-LVDS Differential Data Input12Rxin1+1+LVDS Differential Data Input13GNDPGround14Rxin2-1-LVDS Differential Data Input15Rxin2+1+LVDS Differential Data Input16GNDPGround17RxCLK-1-LVDS Differential Data Input18RxCLK+1+LVDS Differential Data Input19GNDPGround20Rxin3-1-LVDS Differential Data Input21Rxin3+1+LVDS Differential Data Input22GNDPGround23NC-No Connection	11
12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	
13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	12
14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	=
15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	13
16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	14
17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	15
18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	16
19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	17
20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	18
21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	19
22GNDPGround23NC-No Connection	20
23 NC - No Connection	21
	22
2/ NC Ne Connection	23
24 NC - No Connection	24
25 GND P Ground	25
26-29 NC - No Connection	26-29
30 GND P Ground	30
31 LED- P LED Cathode	31
32 LED- P LED Cathode	32
33 NC - No Connection	33
34 NC - No Connection	34
35 NC - No Connection	35
36 NC - No Connection	36
37 NC - No Connection	37
38 NC - No Connection	38
39 LED+ P LED Anode	39
40 LED+ P LED Anode	10

Note 1. I: input, P: Power



10.2 Touch panel assignment

PIN NO.	SYMBOL	DESCRIPTION	NOTE
1	USB_GND	USB_Ground	
2	USB_VDD	USB Power for CTP, 5.0V	Note 1
3	USB_D-	USB _Data Signal –	
4	USB_D+	USB _Data Signal +	
5	I2C_GND	I2C _ Ground	
6	I2C_VDD	I2C _Power for CTP, 3.3 V	Note 1
7	I2C_RST	I2C _Reset Pin, Active low	
8	I2C_SCL	I2C _Clock Input	Note 2
9	I2C_INT	I2C _Interrupt Signal from CTP, Active low	
10	I2C_SDA	I2C _Data Signal	

Note 1. Please do not supply power to both USB_VDD and I2C_VDD at the same time, Otherwise, there is a risk that the LDO on the PCAP FPC will be destroyed.

Note 2. External pull-up resistors are required.

10.3 CON1 assignment

Matched PCAP USB programing cable accessory: RVA-PCAP-USB-CABLE

PIN NO.	SYMBOL	DESCRIPTION	NOTE
1	USB_VDD	USB_Power for CTP, DC 5.0V	Note 1
2	USB_D-	USB _Data Signal -	
3	USB_D+	USB _Data Signal +	
4	USB_GND	USB_Ground	

Note 1. If USD_VDD of CON1 is powered, please make sure to disconnect the PCAP FPC from the PCAP ZIF connector on your application.

Otherwise, there is a risk that the LDO on the PCAP FPC will be destroyed.

10.4 CON2 assignment

PIN NO.	SYMBOL	DESCRIPTION	NOTE
1	I2C_GND	I2C _ Ground	
2	I2C_VDD	I2C _Power for CTP, 3.3 V	
3	I2C_RST	I2C _Reset Pin, Active low	
4	I2C_SCL	I2C _Clock Input	Note 1
5	I2C_INT	I2C _Interrupt Signal from CTP, Active low	
6	I2C_SDA	I2C _Data Signal	

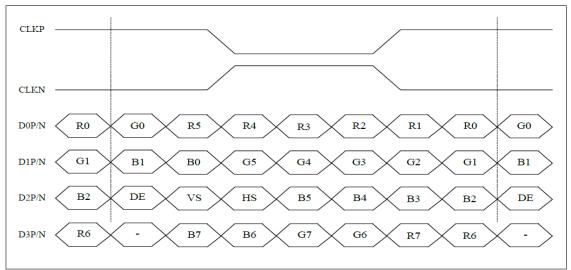
Note 1. External pull-up resistors are required.



11. TIMING CHARACTERISTICS

11.1 LVDS interface characteristic

VESA Format: 8-bit LVDS input, (LVBIT=H, LVFMT=H)



Note 1: Control signals DE: Active high VS HS: Active Low

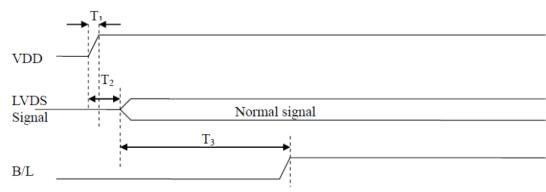
11.2 Timing table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Clock Frequency	FDCLK	66.3	72.4	78.9	MHz
(Rate=60Hz (LVDS))					
HSYNC Period Time	T _H	1380	1440	1500	DCLK
Horizontal Display area	T _{HD}		1280		DCLK
Hsync pulse Width	T _{HPW}	1	-	40	Тс
Hsync Back Porch	T _{HBP}	88	88	88	DCLK
(With pulse width)					
Hsync Front Porch	T _{HFP}	12	72	132	DCLK
VSYNC Period Time	Tv	824	838	872	
Vertical Display area	T _{VD}		800		Н
Vsync pulse Width	Tvw	1	-	20	
Vsync Back Porch	T _{VBP}	23	23	23	1
(With pulse width)					
Vsync Front Porch	T _{VFP}	1	15	49	



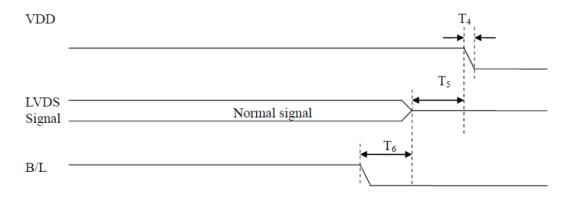
11.3 Power ON/OFF sequence





PARAMETER		UNIT		
	MIN.	TYP.	MAX.	
П	0.5	2	10	
T2	0	5	50	ms
T3	130	136	210	

11.3.2 Power off sequence



PARAMETER	VALUE			UNIT
	MIN.	TYP.	MAX.	
T4	0.5	2	10	ms
T5	0	7	50	
T6	0	2	100	



12.CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

12.1 Mechanical characteristics

DESCRIPTION	SPECIFICATION	REMARK
Touch Panel Size	10.1 inch	
Outline Dimension of CTP	257.96 mm x 168.60 mm	
Product Thickness	2.03mm	
Glass Thickness	1.1 mm	
CTP View Area	217.96 mm x 136.60 mm	uxTouch
Sensor Active Area	218.96 mm x 137.60 mm	
Surface Hardness	7H	

12.2 Electrical characteristics

DESCRIPTION		SPECIFICATION
Power Consumption (IDD)	Active Mode	90 mA
	Sleep Mode	10 mA
Linearity		+/- 1.5mm
Controller		ILI2132A
Resolution		1280 x 800



13. INSPECTION

Standard acceptance/rejection criteria for TFT module

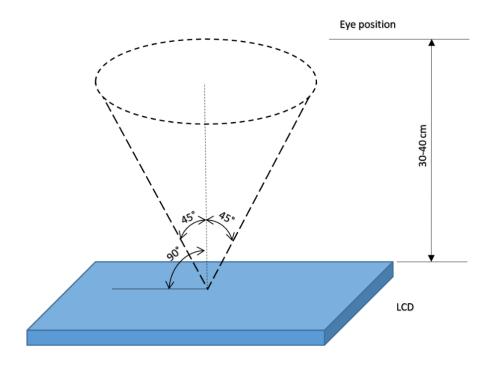
13.1 Inspection condition

Ambient conditions:

- Temperature: 25 ± 2°C
- Humidity: (60 ± 10) %RH
- Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

Viewing distance: 35 ± 5cm between inspector bare eye and LCD.

Viewing Angle: U/D: 45°/45°, L/R: 45°/45°





13.2 Inspection standard

The LCD TFT has zero bad pixels. Please refer the item "Bright/Dark dots".

ITEM			CRITE	RIO	١		
			Size = 10.1"				
Black spots,			Average Diameter		Qualified Qty		
white spots, light leakage, Foreign Particle			D ≤ 0.2 m	nm		lgn	ored
(round Type)			0.2 mm <	: D ≤ C).3 mm	N≤4	4
	D=(x+y)/2 Spots density: 10 n	nm	0.5mm <	D		Not	allowed
	Width 				Size = 10	.1"	
LCD black spots,			Lengt	h	Width		Qualified Qty
white spots, light leakage	Length	-	-		W ≤ 0.0	5	Ignored
(line Type)			L ≤ 5.0	≤ 5.0 0.05< W ≤		0.1	N≤3
	 Spots density: 10 n	nm	5.0 <	L	0.10< W 5.0 < L		Not allowed
	Si		Size =	Size = 10.1			
	ltem			Qualified Qty			
Bright/Dark	Bright dots					0	
Dots	Dark dots					0	
	Cluster Bright Dots or Dark Dots					0	
	Total Bright and Dark Dots		Dots	0			
	Size ≥ 5.0"						
	Average [Qual	ified	Qty
	D < 0.2 mm			Ignored			
Clear spots	0.2 mm < D < 0.3 mm			4			
	0.3 mm < D < 0.5 mm			2			
	0.5 mm < D			0			
	Spots density: 10 mm Size ≥ 5.0"						
	Διγρησιο	Diameter	51282	2 5.0 Qualified Qty			
Touch panel	Average Diameter D < 0.25 mm			Ignored			
spots	0.25 mm < D < 0.5 mm			4			
	0.5 mm < D			0			
				≥ 5.0"			
Touch panel	Length	Wic	dth	Qualified Qty			
white line	-	W < (< 0.03 Ignored			d	
scratch	L < 5.0 0.03 < W < 0.05			2			
	-	- 0.05 < W		0			



14. RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	NOTE
1	High Temperature Storage	80°C/120 hours	
2	Low Temperature Storage	-30°C/120 hours	
3	High Temperature Operating	70 °C /120 hours	Note 1
4	Low Temperature Operating	-20°C/120 hours	
5	High Temperature and High Humidity	Humidity 40°C, 90%RH, 120Hrs	
6	Thermal Cycling Test (No operation)	-20°C for 30min, 70°C for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note 2
7	Vibration Test	Frequency: 10 ÷ 55 Hz. Stroke: 1.5 mm. Sweep: 10Hz ÷ 55Hz ÷ 10 Hz. 2 hours for each direction of X, Y, Z (Total 6 hours)	
8	Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces	

Note 1. Sample quantity for each test item is 5 ÷ 10 pcs.

Note 2. Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.



15.LEGAL INFORMATION

CE marking is usually obligatory only for a complete end product. Riverdi display modules are semi-finished goods which are used as inputs to become part of the finished products.

Therefore, Riverdi display modules are not CE marked.

Riverdi grants the guarantee for the proper operation of the goods for a period of 12 months from the date of possession of the goods. If in a consequence of this guaranteed execution the customer has received the defects-free item as replacement for the defective item, the effectiveness period of this guarantee shall start anew from the moment the customer receives the defects-free item.

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